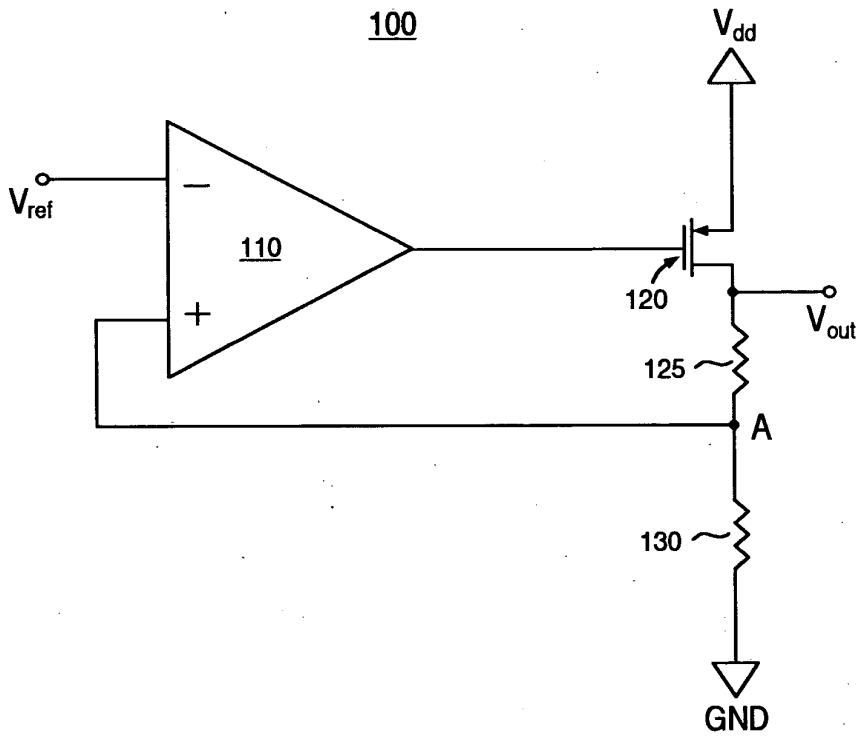

Fig. 1
(Prior Art)

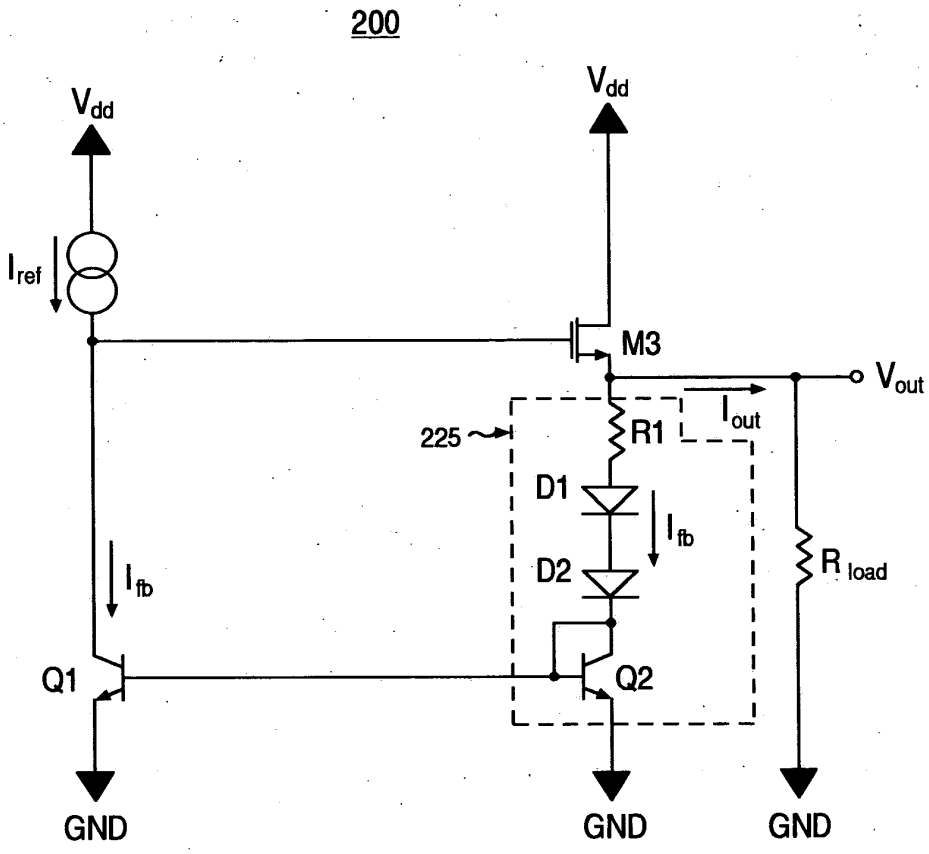
200
V_{dd}
V_{dd}
I_{ref}
M3
V_{out}
I_{out}
225
R1
D1
I_{fb}
D2
I_{fb}
R_{load}
Q1
Q2
GND
GND
GND

Fig. 2

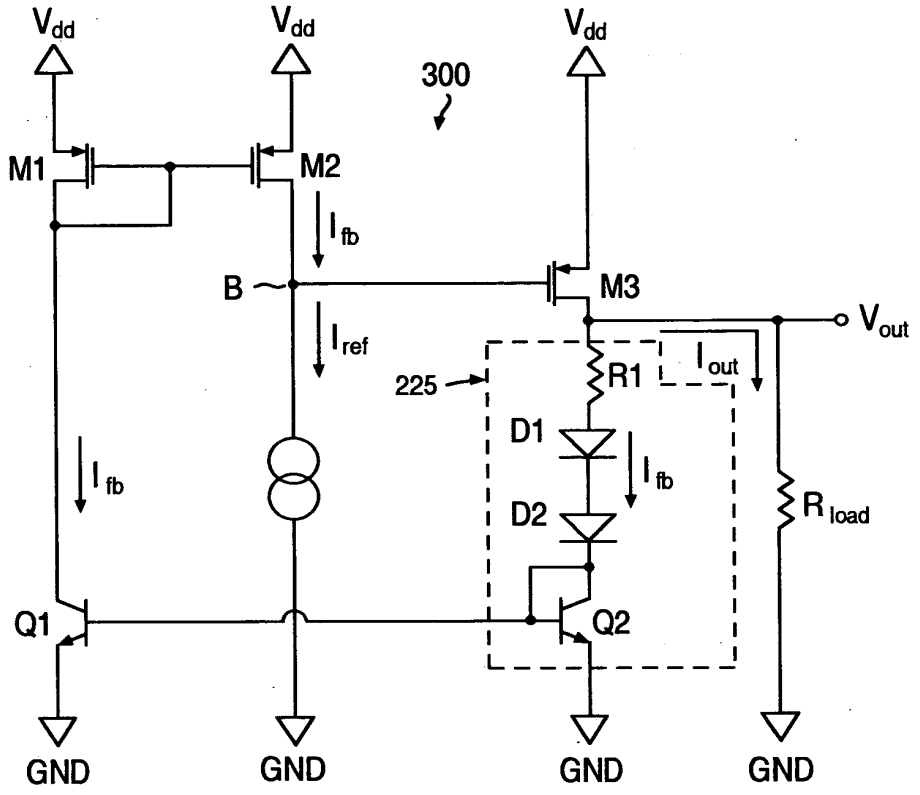
Vdd
Vdd
Vdd
300
M1
M2
Ifb
B
M3
Vout
Iref
225
R1
Iout
D1
Ifb
Ifb
D2
Rload
Q1
Q2
GND
GND
GND
GND

Fig. 3

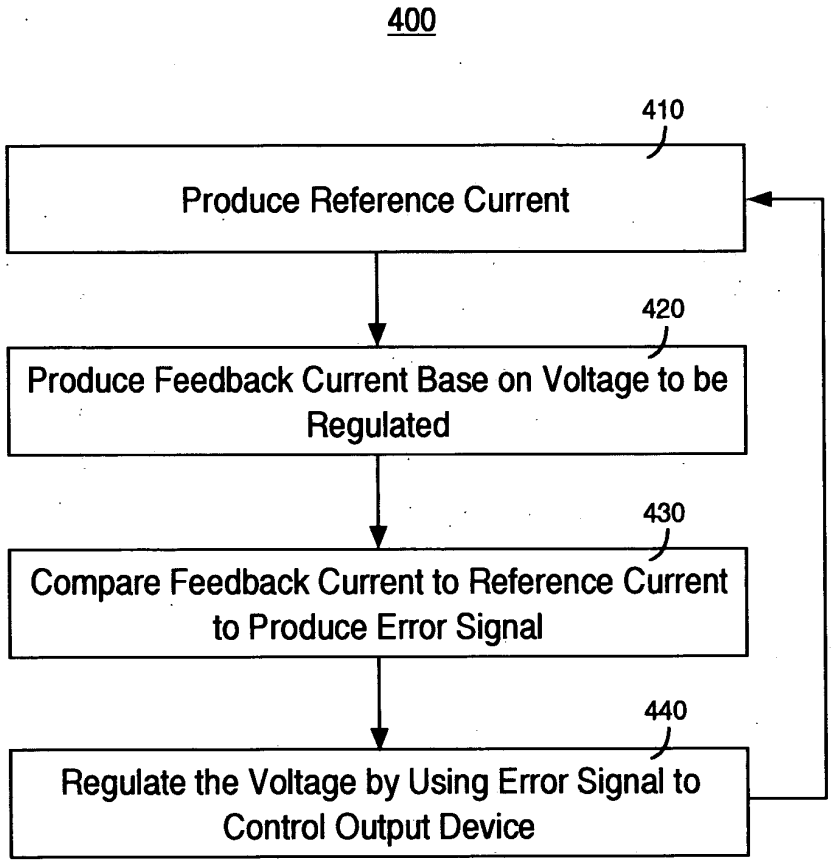
400
410
Produce Reference Current
420
Produce Feedback Current Base on Voltage to be Regulated
430
Compare Feedback Current to Reference Current to Produce Error Signal
440
Regulate the Voltage by Using Error Signal to Control Output Device

Fig. 4